

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,221,229 B2
APPLICATION NO. : 10/821681
DATED : May 22, 2007
INVENTOR(S) : Schrodinger

Page 1 of 4

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Delete Title page illustrating a figure, and substitute therefor, new Title page illustrating a figure. (attached)

(12) **United States Patent**
Schrodinger

(10) **Patent No.:** **US 7,221,229 B2**
(45) **Date of Patent:** **May 22, 2007**

(54) **RECEIVER CIRCUIT HAVING AN OPTICAL
RECEPTION DEVICE**

(75) **Inventor:** Karl Schrodinger, Berlin (DE)

(73) **Assignee:** Finisar Corporation, Sunnyvale, CA
(US)

(*) **Notice:** Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) **Appl. No.:** 10/821,681

(22) **Filed:** Apr. 9, 2004

(65) **Prior Publication Data**
US 2005/0168289 A1 Aug. 4, 2005

Related U.S. Application Data
(60) Provisional application No. 60/540,870, filed on Jan. 30, 2004.

(51) **Int. Cl.**
H03F 3/08 (2006.01)
H03G 3/20 (2006.01)

(52) **U.S. Cl.** 330/308; 250/214 A; 250/214 AG

(58) **Field of Classification Search** None
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

6,933,786 B1* 8/2005 Mohandas et al. 330/308

6,943,630 B2* 9/2005 Forsberg 330/308
2005/0046482 A1* 3/2005 Schrodinger 330/308
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OTHER PUBLICATIONS

Mullrich et al.: "High-Gain Transimpedance Amplifier In InP-Based HBT Technology For The Receiver In 40-Gb/s Optical-Fiber TDM Links", IEEE Journal of Solid State Circuits, vol. 35, No. 9, Sep. 2000, pp. 1260-1265.

Kressel: "Semiconductor Devices For Optical Communication", Topics in Applied Physics, vol. 39, Springer Verlag 1982, 7 pages.

* cited by examiner

Primary Examiner—Robert Pascal

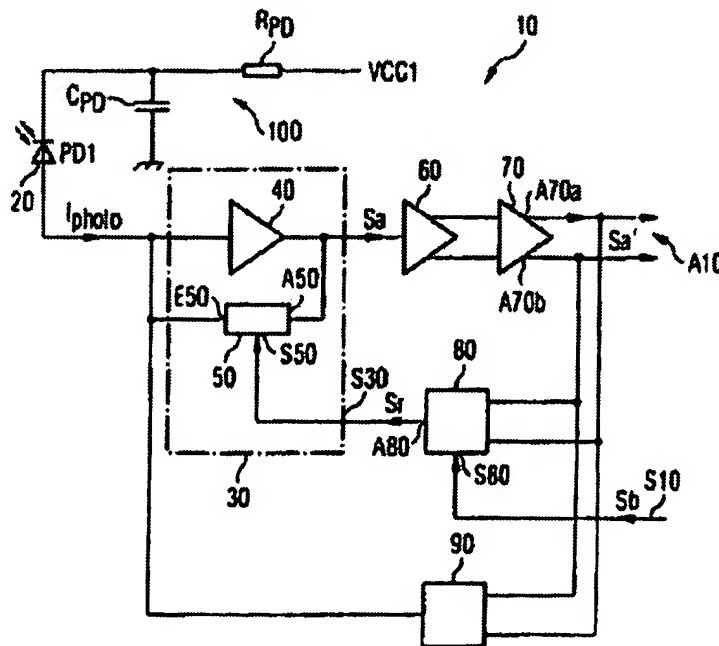
Assistant Examiner—Krista Flanagan

(74) *Attorney, Agent, or Firm*—Workman Nydegger

(57) **ABSTRACT**

A receiver circuit having an optical reception device and having an amplifier connected to the reception device, the amplifier also having a circuit for setting the operating point of the amplifier and also at least one control terminal of the circuit, by which the operating point of the amplifier can be selectively changed between at least two values at the user end. The receiver circuit according to the invention enables a noise optimization of the amplifier by virtue of an adjustability of the operating point of the amplifier.

21 Claims, 4 Drawing Sheets



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Drawings

Sheet 1, replace Figure 1 with the figure depicted herein below, in which the "low-pass filter" has been labeled with --100-- and the "amplified output signal" has been labeled with --Sa'--

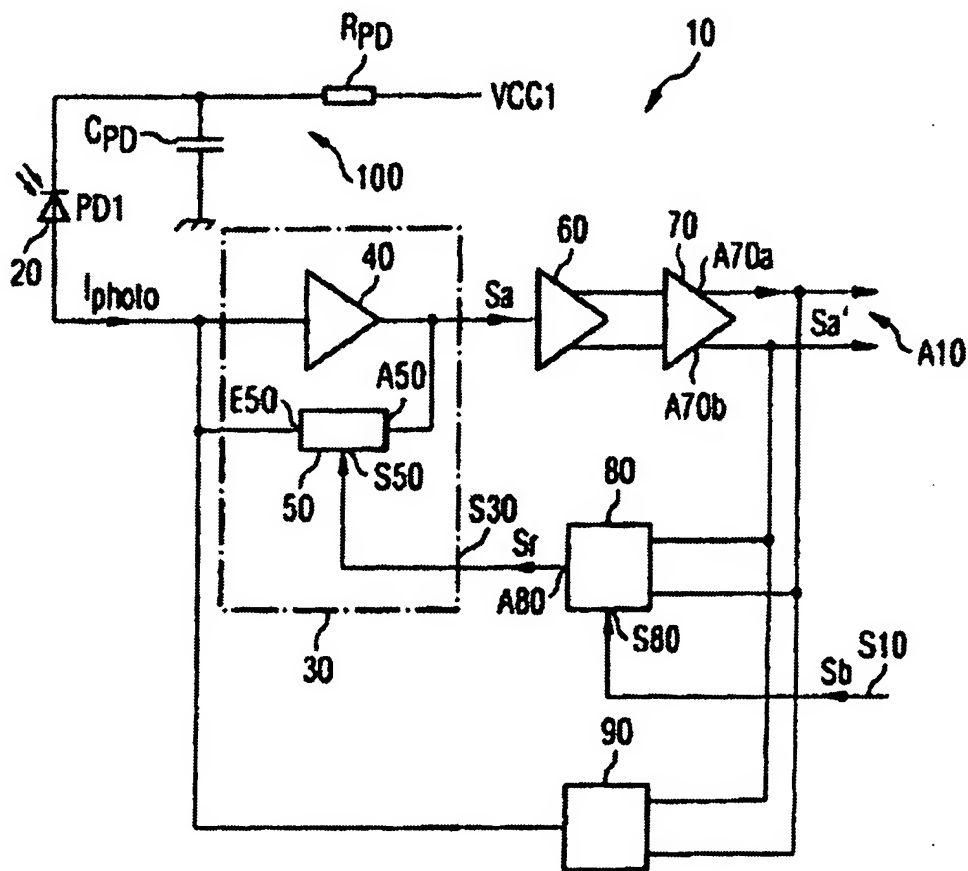


Fig. 1

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Column 7

Line 36, change "transistor" to --transistors--

Column 8

Line 64, change "310" to --210--

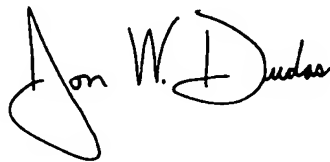
Column 9

Line 2, change "320" to --220--

Line 9, change "320" to --220--

Signed and Sealed this

Twenty-sixth Day of August, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office